

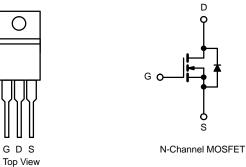
## N-Channel 30-V (D-S) MOSFET

D

S

PRODUCT SUMMARY	
V <sub>DS</sub> (V)	30
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 V$	0. 002
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 V$	0. 004
I <sub>D</sub> (A)	180
Configuration	Single

#### TO-220AB



## **FEATURES**

- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> and UIS Tested
  Compliant to RoHS Directive 2011/65/EU

## **APPLICATIONS**

- OR-ing •
- Server
- DC/DC

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
	T <sub>C</sub> = 25 °C		180	A	
Continuous Drain Current (T <sub>J</sub> = 175 °C)	T <sub>C</sub> = 70 °C		80 <sup>e</sup>		
Continuous Drain Current (1) = 175 C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	28.8 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		19 <sup>b, c</sup>		
Pulsed Drain Current		I <sub>DM</sub>	380		
Avalanche Current Pulse		I <sub>AS</sub>	36		
Single Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	64.8	V	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	1	90 <sup>a, e</sup>	Δ.	
	T <sub>A</sub> = 25 °C	I <sub>S</sub>	3.13 <sup>b, c</sup>	— A	
	T <sub>C</sub> = 25 °C		250 <sup>a</sup>	w	
	T <sub>C</sub> = 70 °C	P	175		
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	3.75 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		2.63 <sup>b, c</sup>		
Operating Junction and Storage Temperature Ra	ange	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS	IERMAL RESISTANCE RATINGS					
Parameter		Symbol	Тур.	Max.	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	$t \le 10 \text{ sec}$	R <sub>thJA</sub>	32	40	°C/W	
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	0.5	0.6	0,00	

Notes:

a. Based on  $T_C = 25 \text{ °C}$ . b. Surface mounted on 1" x 1" FR4 board.

a. t = 10 sec.
d. Maximum under steady state conditions is 90 °C/W.
e. Calculated based on maximum junction temperature. Package limitation current is 90 A.

COMPLIANT

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static				1	1	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	30			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	L 050		35		
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA		- 7.5		mV/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \ \mu A$	1.0		2.5	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
		$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$			10	μA
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	90			A
	_	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 28.8 A		0.002		0
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 27 A		0.004		Ω
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 28.8 A		160		S
Dynamic <sup>b</sup>						<b>I</b>
Input Capacitance	C <sub>iss</sub>			6750		
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		725		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			370		
Tatal Cata Charge	0	$V_{DS}$ = 15 V, $V_{GS}$ = 10 V, $I_{D}$ = 28.8 A		181	277	
Total Gate Charge	Qg			91.5	133	
Gate-Source Charge	Q <sub>gs</sub>	$V_{\text{DS}}$ = 15 V, $V_{\text{GS}}$ = 4.5 V, $I_{\text{D}}$ = 28.8 A		34		nC
Gate-Drain Charge	Q <sub>gd</sub>			29		
Gate Resistance	R <sub>g</sub>	f = 1 MHz		1.4	2.1	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			18	27	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 0.625 $\Omega$		11	17	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ 24 A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$		70	105	
Fall Time	t <sub>f</sub>			10	70     70       81     277       1.5     133       34     7       1.9     7       .4     2.1       18     27       1     17       70     105       10     15       55     83       80     270       55     83	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 15 V, R <sub>L</sub> = 0.67 Ω		55	83	ns
Rise Time	t <sub>r</sub>			180	270	
Turn-Off Delay Time	t <sub>d(off)</sub>	${\rm I_D}\cong$ 22.5 A, ${\rm V_{GEN}}$ = 4.5 V, ${\rm R_g}$ = 1 $\Omega$		55	83	
Fall Time	t <sub>f</sub>			12	18	
Drain-Source Body Diode Characteristic	s		<u> </u>	1	1	
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	T <sub>C</sub> = 25 °C			90	۸
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				90	A
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 22 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			52	78	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	L = 20.4  di/dt = 100.4/m T = 25.90		70.2	105	nC
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = 20 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, \text{ T}_J = 25 \text{ °C}$		27		
Reverse Recovery Rise Time	t <sub>b</sub>			25		ns

Notes:

a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

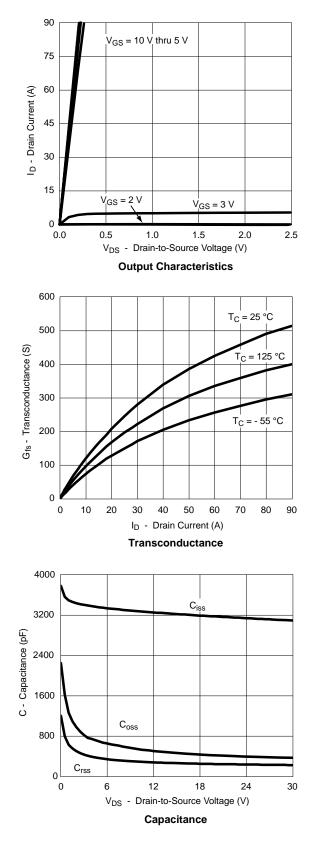
b. Guaranteed by design, not subject to production testing.

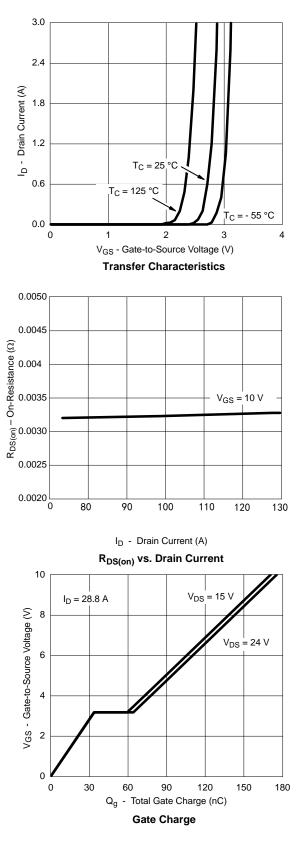
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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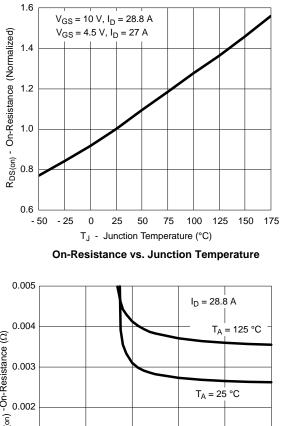


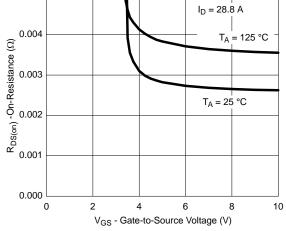


服务热线:400-655-8788

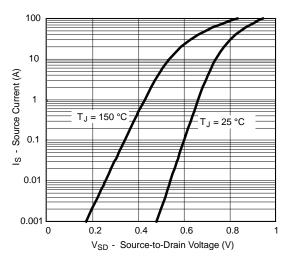


### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

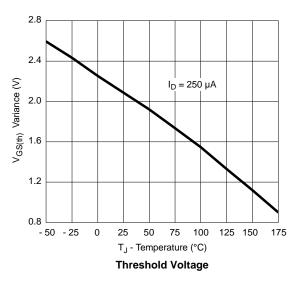


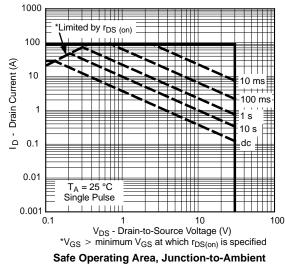


R<sub>DS(on)</sub> vs. V<sub>GS</sub> vs. Temperature

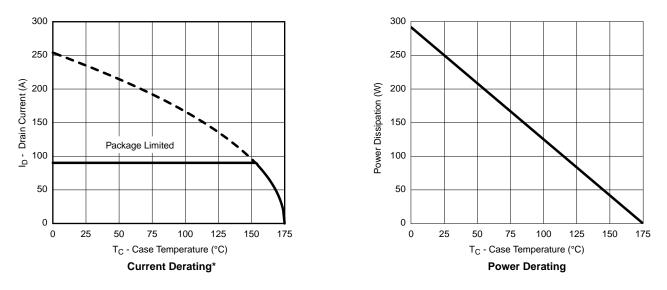


Forward Diode Voltage vs. Temperature



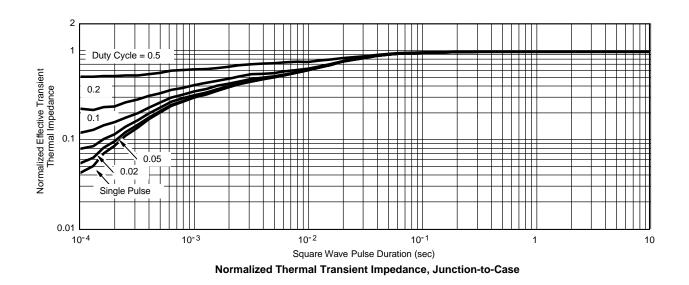






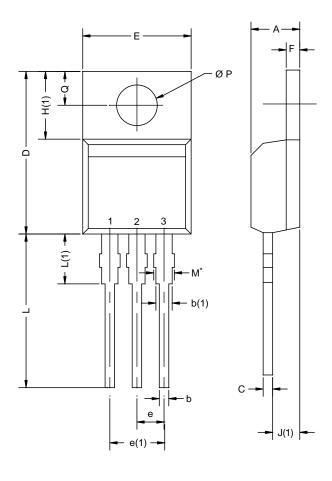
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

\*The power dissipation  $P_D$  is based on  $T_{J(max)} = 175$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





# **TO-220AB**



25     39       20     36       85     1       04     1	MAX.         4.65         1.01         1.73         0.61         15.49         10.51         2.67         5.28	0. 0. 0. 0. 0. 0.	11N.         167         027         047         014         585         395         095	MAX           0.183           0.040           0.068           0.024           0.610           0.414           0.105
39       20       36       85       04       11	1.01 1.73 0.61 15.49 10.51 2.67	0. 0. 0. 0. 0.	027 047 014 585 395	0.040 0.068 0.024 0.610 0.414
20 36 85 1 04 1 11	1.73 0.61 15.49 10.51 2.67	0. 0. 0. 0.	047 014 585 395	0.068 0.024 0.610 0.414
36 85 1 04 1 11	0.61 15.49 10.51 2.67	0. 0. 0.	014 585 395	0.024 0.610 0.414
85 1 04 1 11	15.49 10.51 2.67	0.	585 395	0.610 0.414
04 1 11	10.51 2.67	0.	395	0.414
41	2.67	-		
	-	0.	095	0.105
38	5.28		000	000
	5.20	0.	192	0.208
14	1.40	0.	045	0.055
)9	6.48	0.	240	0.255
11	2.92	0.	095	0.115
35 1	14.02	0.	526	0.552
32	3.82	0.	131	0.150
54	3.94	0.	139	0.155
60	3.00	0.	102	0.118
3	35 2 4 0	35         14.02           2         3.82           4         3.94           0         3.00	35         14.02         0.           2         3.82         0.           4         3.94         0.           0         3.00         0.	35         14.02         0.526           2         3.82         0.131           4         3.94         0.139

#### Notes

\* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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