

P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)		
- 30	0.022 at V _{GS} = - 10 V	- 6.1	5.1 nC		
	0.026 at V_{GS} = - 4.5 V	- 5.1	5.1110		

FEATURES

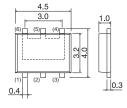
- Halogen-free According to IEC 61249-2-21 Available
- TrenchFET[®] Power MOSFET

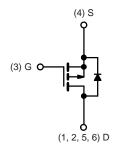
APPLICATIONS

· Load Switch









P-Channel MOSFET

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	- 30	V	
Gate-Source Voltage		V _{GS}	± 20	V	
	T _C = 25 °C		- 6.1		
Continuous Drain Current ($T_1 = 150 \ ^{\circ}C$)	T _C = 70 °C		- 5.1		
Continuous Drain Current $(T_J = 150 \text{ C})$	T _A = 25 °C	I _D	- 4.1 ^{b, c}		
	T _A = 70 °C	1	- 3.3 ^{b, c}	A	
Pulsed Drain Current		I _{DM}	- 20		
Continuous Source-Drain Diode Current	T _C = 25 °C		- 2.5		
	T _A = 25 °C	I _S	- 1.67 ^{b, c}		
Maximum Power Dissipation	T _C = 25 °C		3.0		
	T _C = 70 °C		2.0	w	
	T _A = 25 °C	P _D	2.0 ^{b, c}	VV	
	T _A = 70 °C	1	1.3 ^{b, c}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R _{thJA}	55	62.5	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	34	41	C/VV	

Notes:

a. Based on $T_C = 25 \text{ °C}$. b. Surface Mounted on 1" x 1" FR4 board.

c. t = 5 s.

d. Maximum under Steady State conditions is 110 °C/W.



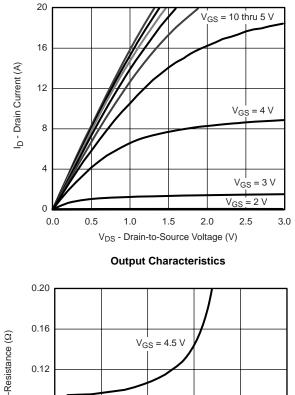
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$			- 31		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = - 250 μA		4.5			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1.0		- 3.0	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V			- 1		
		V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 55 °C			- 10	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, \text{ V}_{GS} = -10 \text{ V}$	- 20			A	
		V _{GS} = - 10 V, I _D = - 4.1 A		0.022		-	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 1.0 A		0.026		Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 4.1 A		8		S	
Dynamic ^b				1	I	1	
Input Capacitance	C _{iss}			450		pF	
Output Capacitance	C _{oss}	V _{DS} = - 15 V, V _{GS} = 0 V, f = 1 MHz		80			
Reverse Transfer Capacitance	C _{rss}			63			
Table Oats Observe	Qg	V_{DS} = - 15 V, V_{GS} = - 10 V, I_{D} = - 4.1 A		10	15	- nC	
Total Gate Charge		V _{DS} = - 15 V, V _{GS} = - 4.5 V, I _D = - 4.1 A		5.1	8		
Gate-Source Charge	Q _{gs}			1.8			
Gate-Drain Charge	Q _{gd}			2.5			
Gate Resistance	Rg	f = 1 MHz		7		Ω	
Turn-On Delay Time	t _{d(on)}			40	60	ns	
Rise Time	t _r	V_{DD} = - 15 V, R_L = 4.6 Ω		80	120		
Turn-Off Delay Time	t _{d(off)}	$\rm I_D$ \cong - 3.3 A, $\rm V_{GEN}$ = - 4.5 V, $\rm R_g$ = 1 Ω		20	30		
Fall Time	t _f			12	20		
Turn-On Delay Time	t _{d(on)}			5	10		
Rise Time	t _r	V_{DD} = - 15 V, R_L = 4.6 Ω		13	20		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 3.3 A, V_GEN = - 10 V, R_g = 1 Ω		20	30		
Fall Time	t _f			10	15		
Drain-Source Body Diode Characteristic	s			•			
Continuous Source-Drain Diode Current	ا _S	T _C = 25 °C			- 2.5	•	
Pulse Diode Forward Current ^a	I _{SM}				- 20	A	
Body Diode Voltage	V _{SD}	I _S = - 3.3 A		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			20	30	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	L = 2.2.4 di/dt = 100.4/tra T = 05.90		20	30	nC	
Reverse Recovery Fall Time	t _a	$I_F = -3.3 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 \text{ °C}$		14			
Reverse Recovery Rise Time	t _b			6		ns	

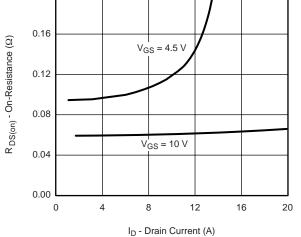
Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 % b. Guaranteed by design, not subject to production testing.

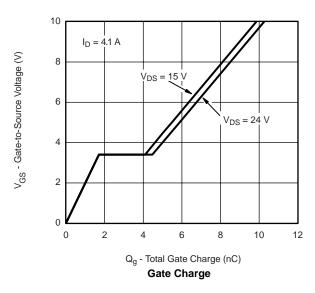
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

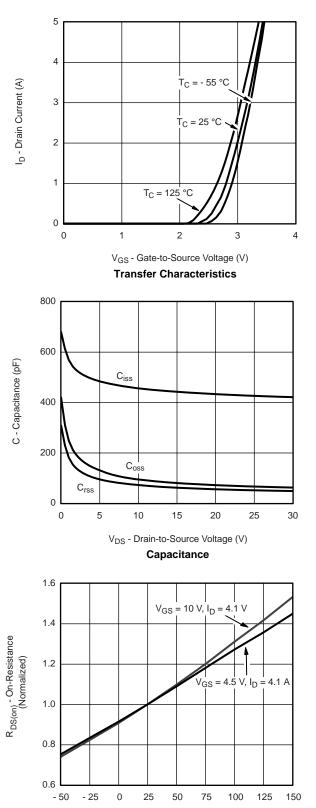






On-Resistance vs. Drain Current and Gate Voltage

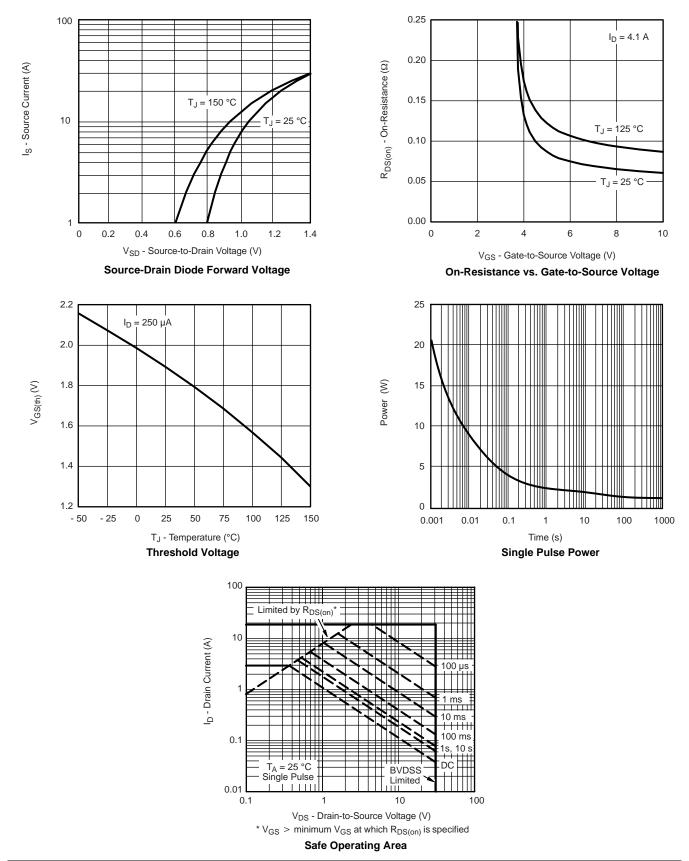




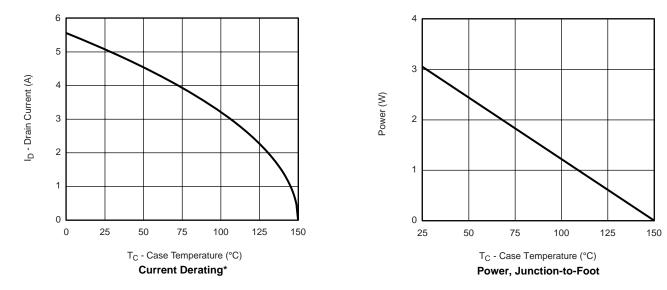
T_J - Junction Temperature (°C)

On-Resistance vs. Junction Temperature



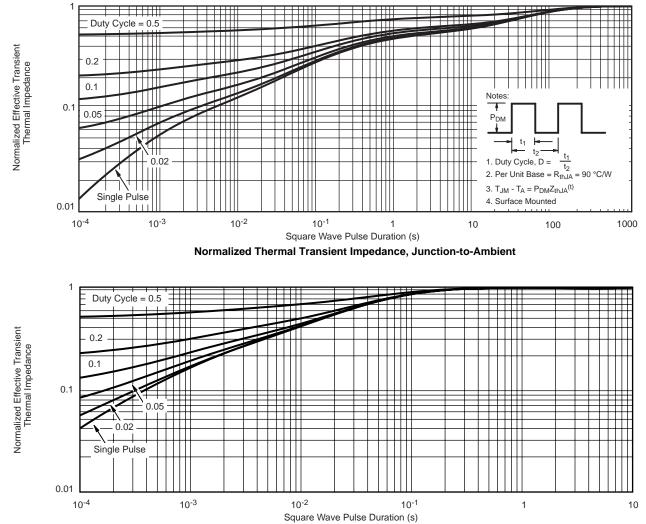






* The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.









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