

# N-Channel 30 V (D-S) MOSFET

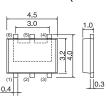
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A) <sup>a, e</sup>	Q <sub>g</sub> (Typ.)		
30	0.023 at V <sub>GS</sub> = 10 V	6	4.2 nC		
	0.027 at V <sub>GS</sub> = 4.5 V	6	4.2110		

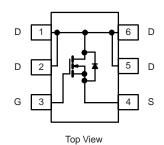
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFET
- Low On-Resistance
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC



#### Dimensions (Unit: mm)





## **APPLICATIONS**

• DC/DC Converters, High Speed Switching

ABSOLUTE MAXIMUM RATIN	IGS (1 <sub>A</sub> = 25 °C	, unless otherwi	se notea)		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30	V	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
	T <sub>C</sub> = 25 °C		6 <sup>e</sup>		
Continuous Proin Current (T = 150 °C)	T <sub>C</sub> = 70 °C		6 <sup>e</sup>		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	5.5 <sup>b, c</sup>		
	T <sub>A</sub> = 70 °C		4.4 <sup>b, c</sup>	A	
Pulsed Drain Current (t = 300 μs)		I <sub>DM</sub>	25		
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C		2.1		
	T <sub>A</sub> = 25 °C	I <sub>S</sub>	1.1 <sup>b, c</sup>		
	T <sub>C</sub> = 25 °C		2.5		
Maximum Power Dissipation	T <sub>C</sub> = 70 °C		1.6	W	
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.3 <sup>b, c</sup>	VV	
	T <sub>A</sub> = 70 °C		0.8 <sup>b, c</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub> - 55 to 150		90	
Soldering Recommendations (Peak Temperature)			260	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Typical Maximum		
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 5 s	R <sub>thJA</sub>	75	100	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	40	50		

#### Notes:

- a. Based on  $T_C$  = 25 °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c t = 5 s
- d. Maximum under steady state conditions is 166 °C/W.
- e. Package limited.



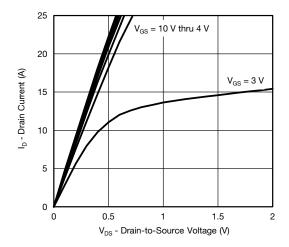
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static						-1	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V, I}_{D} = 250 \mu\text{A}$	30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = 250 μA		30		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_{J}$	1 <sub>D</sub> = 230 μA		- 4.8			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	1.2		2.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			1		
		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70 °C			10	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α	
Dania Carana Car Olata Daniata and	В	$V_{GS} = 10 \text{ V}, I_D = 5.5 \text{ A}$		0.023			
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 5 \text{ A}$		0.027		Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 5.5 A		24		S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			424			
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		100		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			42			
Total Gate Charge	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 5.5 \text{ A}$		8.2	13	nC	
				4.2	7		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 5.5 \text{ A}$		1.4			
Gate-Drain Charge	Q <sub>gd</sub>			1.4			
Gate Resistance	R <sub>g</sub>	f = 1 MHz	2.5	12.6	25.2	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			6	12	ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 3.4 $\Omega$		20	30		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 4.4 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		14	21		
Fall Time	t <sub>f</sub>			10	20		
Turn-On Delay Time	t <sub>d(on)</sub>			3	6		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 3.4 $\Omega$		11	20		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 4.4 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		20	30		
Fall Time	t <sub>f</sub>			7	14		
<b>Drain-Source Body Diode Characteristi</b>	cs		<u> </u>				
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			2.1	Λ	
Pulse Diode Forward Current	I <sub>SM</sub>				25	A	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 4.4 A, V <sub>GS</sub> = 0 V		0.82	1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			13	20	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 4.4 A, dI/dt = 100 A/μs, T <sub>.I</sub> = 25 °C		6	12	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	1F - 4.4 A, αι/αι = 100 A/μs, 1J = 25 °C		8		20	
Reverse Recovery Rise Time t <sub>b</sub>				5		ns	

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
  b. Guaranteed by design, not subject to production testing.

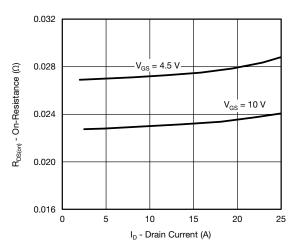
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



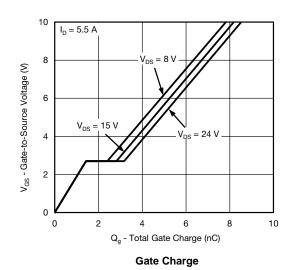
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



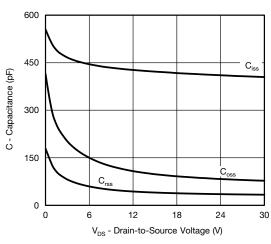
#### **Output Characteristics**



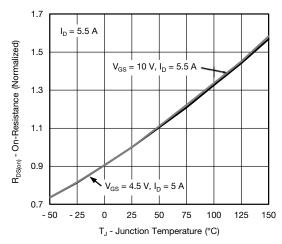
On-Resistance vs. Drain Current and Gate Voltage



**Transfer Characteristics** 



Capacitance

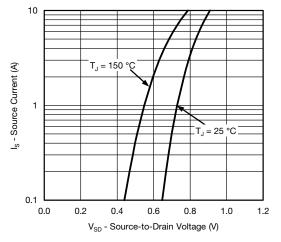


On-Resistance vs. Junction Temperature

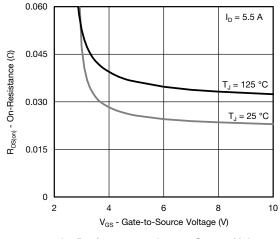
4



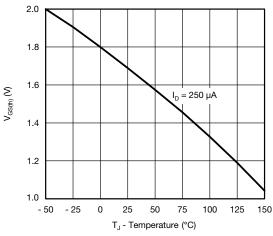
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



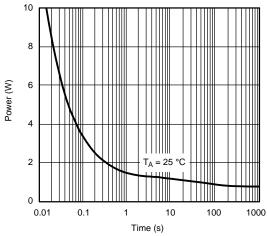
#### Source-Drain Diode Forward Voltage



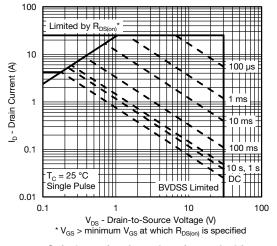
On-Resistance vs. Gate-to-Source Voltage



**Threshold Voltage** 



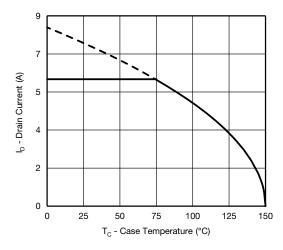
Single Pulse Power (Junction-to-Ambient)



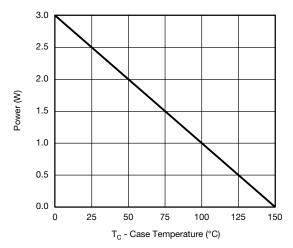
Safe Operating Area, Junction-to-Ambient



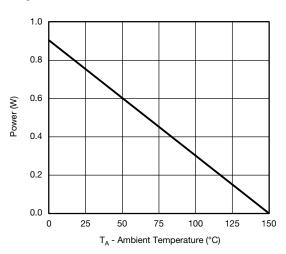
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



#### **Current Derating\***





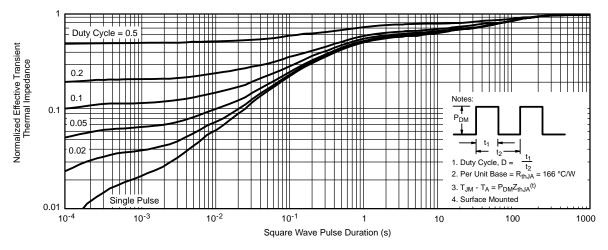


**Power Derating, Junction-to-Ambient** 

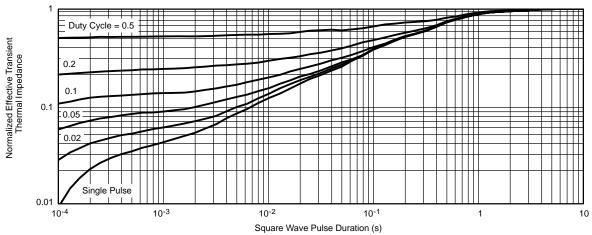
<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max.)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



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